Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A digital television system comprising:

a first and second housing;

a receiver to receive a digital television signal in said first housing;

a digital television display in said second housing; and

a digital graphics bus coupled to said receiver in said first housing and said display in said second housing to transmit processed video data in a digital format from said first housing to said second housing, wherein the digital graphics bus comprises a first transition minimized differential signaling (TDMS) link and a second TDMS link.

Claim 2 (previously presented): The system of claim 1 wherein said first housing is part of a modular platform including a plurality of replaceable cards.

Claim 3 (original): The system of claim 2 wherein each of said cards is received in a plug, said plugs for said cards coupled by a bus.

Claim 4 (previously presented): The system of claim 1 wherein said digital graphics bus is coupled to an encryption engine to encrypt the processed video data before it is transmitted across said digital graphics bus.

Claim 5 (original): The system of claim 2 wherein one of said cards is a motherboard including a processor.

Claim 6 (original): The system of claim 5 wherein another of said cards is a television tuner/capture card.

Claim 7 (canceled)

Claim 8 (original): The system of claim 2 including plugs in said platform for both power and data.

Claim 9 (previously presented): The system of claim 8 wherein said plugs are to receive two different types of serial bus interfaces.

Claim 10 (canceled)

Claim 11 (currently amended): An apparatus comprising:

an encryption engine in a digital television receiver coupled to a digital graphics bus to encrypt digital signals transferred from said digital television receiver to said digital graphics bus,

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said encryption engine to provide two different levels of encryption via a linear feedback shift register; and

~~a decryption engine in a digital television display coupled to said digital graphics bus to decrypt the digital signals transferred from said digital graphics bus to said digital television display via a linear feedback shift register.~~

Claim 12 (canceled)

Claim 13 (previously presented):   The apparatus of claim 11 wherein said digital graphics bus is to periodically encrypt at a higher level of encryption.

Claim 14 (previously presented):   The apparatus of claim 13 wherein the level of encryption is to change on frame boundaries.

Claim 15 (cancel)

Claim 16 (currently amended): The apparatus of claim 11 wherein said linear feedback shift ~~registers include~~ register includes a programmable tap ~~registers~~ register.

Claim 17 (currently amended): The apparatus of claim 16 wherein said programmable tap ~~registers are~~ register is to receive an external tap selection input ~~signals~~ signal.

Claim 18 (previously presented):   The apparatus of claim 17 including a combiner to combine a seed signal together with feedback from said programmable tap register to create an input signal to said linear feedback shift register.

Claim 19 (currently amended): The apparatus of claim 18 wherein said programmable tap register includes combinatorial logic and tap memory.

Claim 20 (currently amended): The apparatus of claim 11 including a decryption engine and an encryption engine on both ends of said digital graphics bus.

Claim 21 (previously presented):   The apparatus of claim 11 wherein said digital graphics bus is to transfer streaming video at 100 megahertz or higher.

Claims 22-28 (canceled)

Claim 29 (previously presented):   A method of implementing a digital television system comprising:

receiving a digital television signal with a receiver in a first housing;

transmitting encrypted video signals between said first housing and a second housing coupled to said first housing, said second housing including a display; and

periodically changing the level of encryption of said encrypted video signals using a linear feedback shift register.

Claim 30 (original):   The method of claim 29 wherein changing the level of encryption includes changing the level of encryption on frame boundaries.

Claim 31 (previously presented):   The method of claim 29 further comprising transmitting the encrypted video signals via a digital graphics bus.

Claim 32 (cancel)

Claim 33 (currently amended): The system of claim [[32]] 1, wherein the first TDMS link is to transmit reduced blanking interval data.

Claim 34 (previously presented):   The apparatus of claim 11, wherein the digital graphics bus comprises a first transition minimized differential signaling (TDMS) link and a second TDMS link.

Claim 35 (previously presented):   The apparatus of claim 34, wherein the first TDMS link is to transmit reduced blanking interval data.

Claim 36 (previously presented):   The system of claim 5, wherein another of said cards includes a digital video storage.

Claim 37 (new):   The system of claim 1, wherein the system is to transmit the processed video data only on the first TDMS link based on a type of the digital television display.

Claim 38 (new):   The system of claim 37, wherein the system is to transmit the processed video data only on the first TDMS link, if the digital television display supports a high definition format having a reduced blanking interval.

Claim 39 (new):   The system of claim 1, wherein the first TDMS link is to transmit odd pixel data and the second TDMS link is to transmit even pixel data.

Claim 40 (new):   The system of claim 1, further comprising a transceiver in the first housing to communicate with a remote control device, wherein the first housing is to control the digital television display responsive to input from the remote control device.

Claim 41 (new):   The apparatus of claim 11, further comprising a decryption engine in a digital television display coupled to said digital graphics bus to decrypt the digital signals transferred from said digital graphics bus to said digital television display via a linear feedback shift register.

Claim 42 (new):   The apparatus of claim 11, wherein the encryption engine further comprises a combiner to couple an output of the linear feedback shift register with color plane data.

Claim 43 (new):   The method of claim 29, further comprising combining an output of the linear feedback shift register with color plane data to obtain the encrypted video signals.